APPENDIX E-055 V

DIRECT DELIVERY CONFIRMATION DOCUMENT VIOLATIONS

1. INPUT DOCUMENT

Appendix B-55, Direct Delivery Confirmation Document, DIC ZCM.

2. INPUT TO PROCEDURE DECISION CHART

Direct Delivery Confirmation Transactions.

- 3. PURPOSE, CONTENTS, AND RESPONSIBILITIES
- a. This appendix is applicable to chapters 3 and 43 and provides instruction for the correction and reentry of Direct Delivery Confirmation Documents which have been rejected from computer processing. Reason for computer rejection, by Violation Reason Code, is listed below. The Requisition Processing Branch within the Directorate of Supply Operations is responsible for processing the violation.
- b. An appendix F-114, Requisition Control and Status Printout, with its Constant ORC 96, will also be routed to the Requisition Processing Branch for violation processing.

VIOLATION

REASON CODE DEFINITION

PC Action to Closeout DVD Requisition with Quantity Adjustment not Processed to Update ARCS.

MC Action on DVD Requisition not Processed to Update ARCS.

- 4. PROCESSING/RESEARCH INSTRUCTIONS
 - a. VRC PC.
 - (1) REASON FOR VIOLATION/ERROR CONDITION

VRC PC will be output when DIC ZCM passed from the Contracting Subsystem cannot be processed in the Distribution Subsystem to update the Active Requisition Control Status (ARCS) record due to one of the following error conditions.

- (a) The DIC ZCM with Status Code BJ does not find a matching ARCS record:
- (b) The DIC ZCM with Status Code BJ matches an ARCS record but there is no Activity (Supply) Section; or
- (c) The DIC ZCM with Status Code BJ matches an ARCS record but the Status Code reflected in the Activity Section is other than BV.

(2) PROCESSING/RESEARCH REQUIRED

The DIC ZCM is generated with VRC PC to provide the appropriate personnel with information indicating a file incompatibility between the Contracting Subsystem and the Distribution Subsystem exists and requires corrective action. Personnel should contact the appropriate personnel in the Information Processing Center (IPC) for assistance. If the IPC is unable to assist, contact the appropriate functional branch in DSIO-M for assistance.

b. VRC MC.

(1) REASON FOR VIOLATION/ERROR CONDITION

VRC MC will be output when DIC ZCM with other than Status Code BJ passed from the Contracting Subsystem cannot be processed in the Distribution Subsystem to update the Active Requisition Control Status (ARCS) record due to one of the following error conditions.

- (a) The DIC ZCM does not match a document number in the ARCS.
- (b) The DIC ZCM matches a document number in the ARCS but there is no Supply (Activity) Section.
- (c) The DIC ZCM matches a document number in the ARCS but the Status Code reflected in the Supply Section is other than BZ or BV.
- (d) The DIC ZCM with Status Code B1 matches an ARCS in Status Code BV and the Estimated Issue Date (EID) is other than a numeric greater than 1
- (e) The DIC ZCM with Status Code ZB, ZF, ZJ, or RB matches an ARCS record and the NSN matches a record with a NIR Key Code D_. A Supply Status document, DIC AE_, with Status Code CA has been mechanically prepared.
 - (f) The DIC ZCM contains an invalid Status Code.
- (g) The DIC ZCM with Status Codes ZB, ZF, ZJ, or RB contains a quantity that is not a numeric greater than 1. A Supply Status document, DIC AE_, with Status Code CA has been mechanically prepared.
- (h) The DIC ZCM with Status Codes ZB, ZF, ZJ, or RB matches an ARCS record with a Confirmation Quantity that satisfies the quantity, either totally or in part, in the Supply Section.

(2) PROCESSING/RESEARCH REQUIRED

The DIC ZCM is generated with VRC MC to provide the appropriate personnel with information indicating a file incompatibility between the Contracting Subsystem and the Distribution Subsystem exists and requires corrective action. Personnel should contact the appropriate personnel in the Information Processing Center (IPC) for assistance. If the IPC is unable to assist, contact the appropriate functional branch in DSIO-M for assistance.

5. REENTRY INSTRUCTIONS

Not applicable to this process.